Source Tests of a Germanium Strip Detector The MARK III Detector:



136Xe + 12C @ 595 MeV $\beta \sim 8.7\%$



Counts per channel







Lithium-Horizontal-Back

109
Cd, 57 Co, 139 Ce, 113 Sn,
 137 Cs, 88 Y and 60 Co
E _{γ} = 88 keV to 1.8 MeV
Calibrated strength



events associated with different multiplicity of strip hits in offline sorting using ROOT



Pulses in regular planar

induced signals at each electrode due to both charge carriers



Small Electrode Effect:

induced signal in an electrode is small, until the charge that it will later collect arrives in its vicinity



Single-polarity charge sensing:

signal from Li is mainly due to electrons signal from B is mainly due to holes

Uses: (i) improvement in performance if one charge carrier subject to large trapping and not the other

(ii) allows measurement of the interaction position by time difference between B and Li signals



Charge collection efficiency:

bulk charge trapping edge effects low field regions charge collection away from electrodes



Spectra as a function of bias voltage at 1408 keV





Resolution as a function of bias:





Drift velocity as a function of applied field:



Applied field



No measurable resolution difference between front and back illumination.....suggests not bulk trapping

Two-Strip Cross Talk

Reconstruct Compton scattered events by summing energies of two-strip events

1 to 3% shift in the gain from one-pixel to adjacent two-pixel events

Non-adjacent strips do not show significant shifts





Schematic Model



capacitive coupling between neighbouring strips

for single strip event

charge amplified is Q - 2q net charge charge stored collected in coupling

for two strip sum adjacent

$$\sim (Q/2 - q/2) + (Q/2 - q/2) = Q - q$$

non-adjacent

 \sim (Q/2 - q) + (Q/2 - q) = Q - 2q











@121 absolute efficiencies 2.19(4)%, similar to Monte Carlo

And Next?

For now: improve multiparameter system to reduce noise understand efficiency (?)

MARK IV Detector is on its way to Argonne.....

5 mm wide guard ring boron side DC coupled lithium side AC coupled cold initial preamp FET stages no boron nitride sandwich

Ultimate goal: MARK V

include improvements to MARK IV streamlined cryostat for use in arrays, close geometries etc.